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APPLICANTS Marvin J. Rich, Poughkeepsie, NY; Ashutosh Misra, Bangalore, INDIA;					
** CONTINUING DATA *****					
** FOREIGN APPLICATIONS *****					
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 02/05/2002					
Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after met <input type="checkbox"/> Allowance Verified and <i>Deash G. Misra</i> Acknowledged <u>Examiner's Signature</u> Initials		STATE OR COUNTRY NY ✓	SHEETS DRAWING 22 ✓	TOTAL CLAIMS 15 ✓	INDEPENDENT CLAIMS 7 ✓
ADDRESS 23334					
TITLE VHDL technology library method for efficient customization of chip gate delays ✓					
FILING FEE RECEIVED 1076	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit		